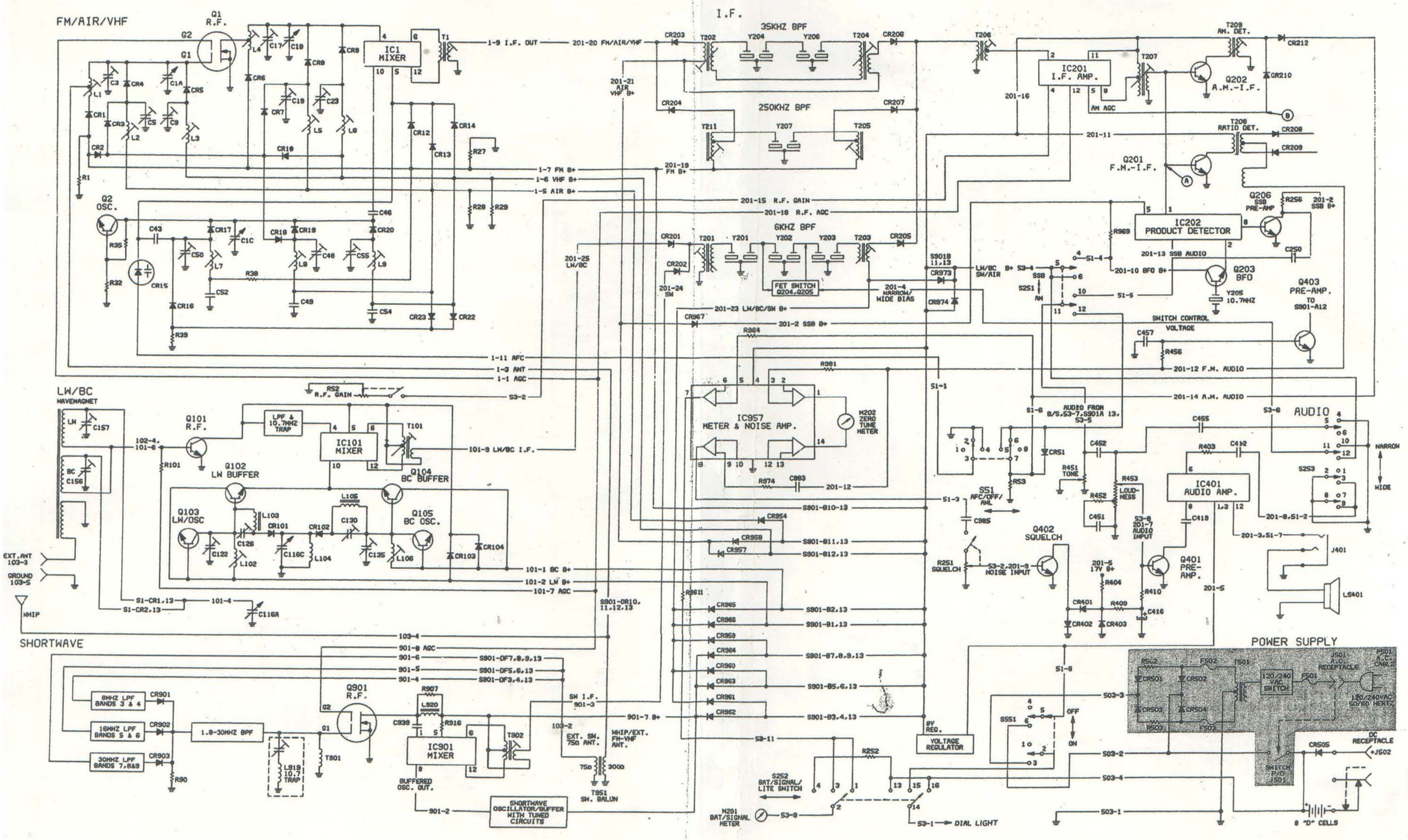
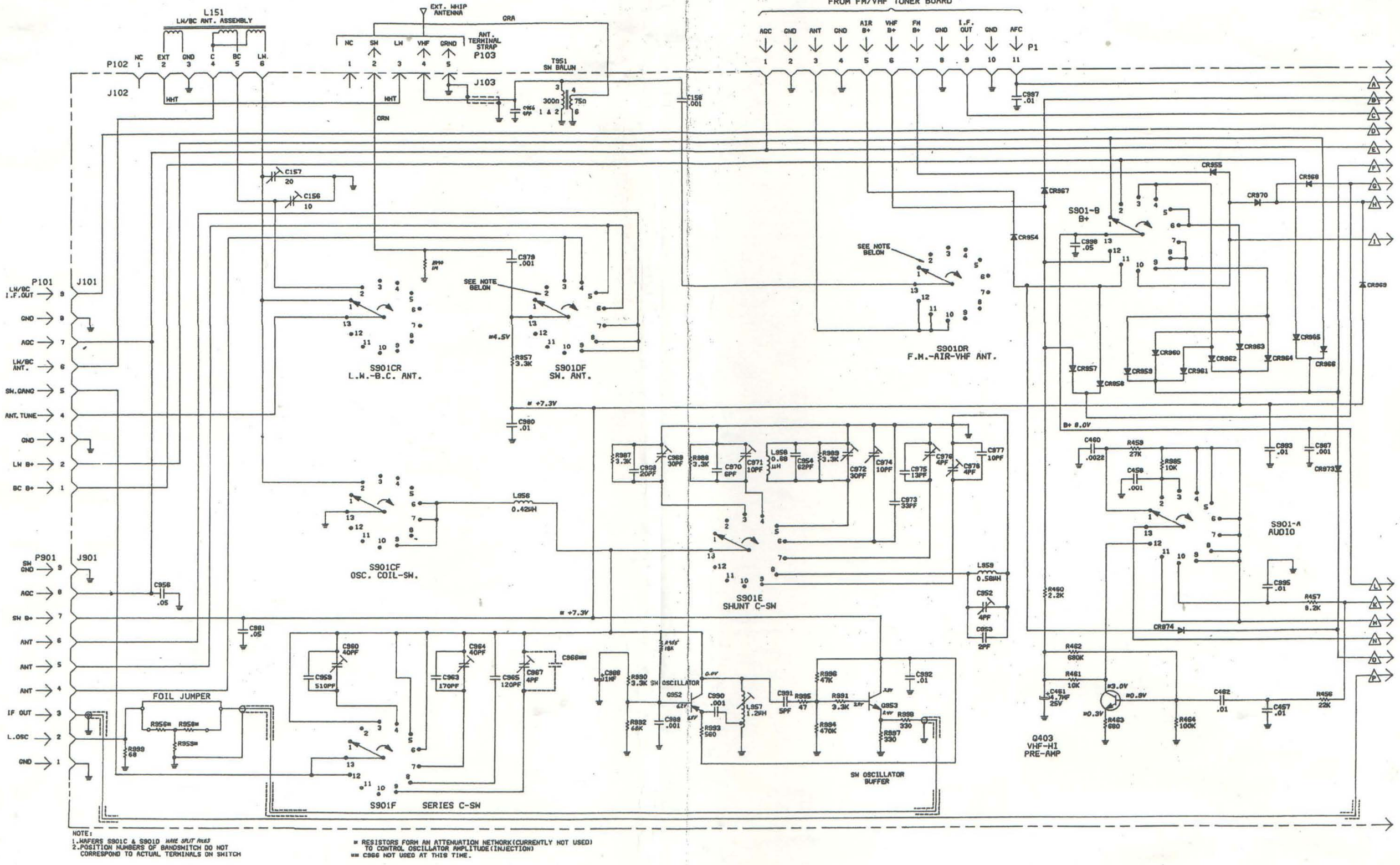


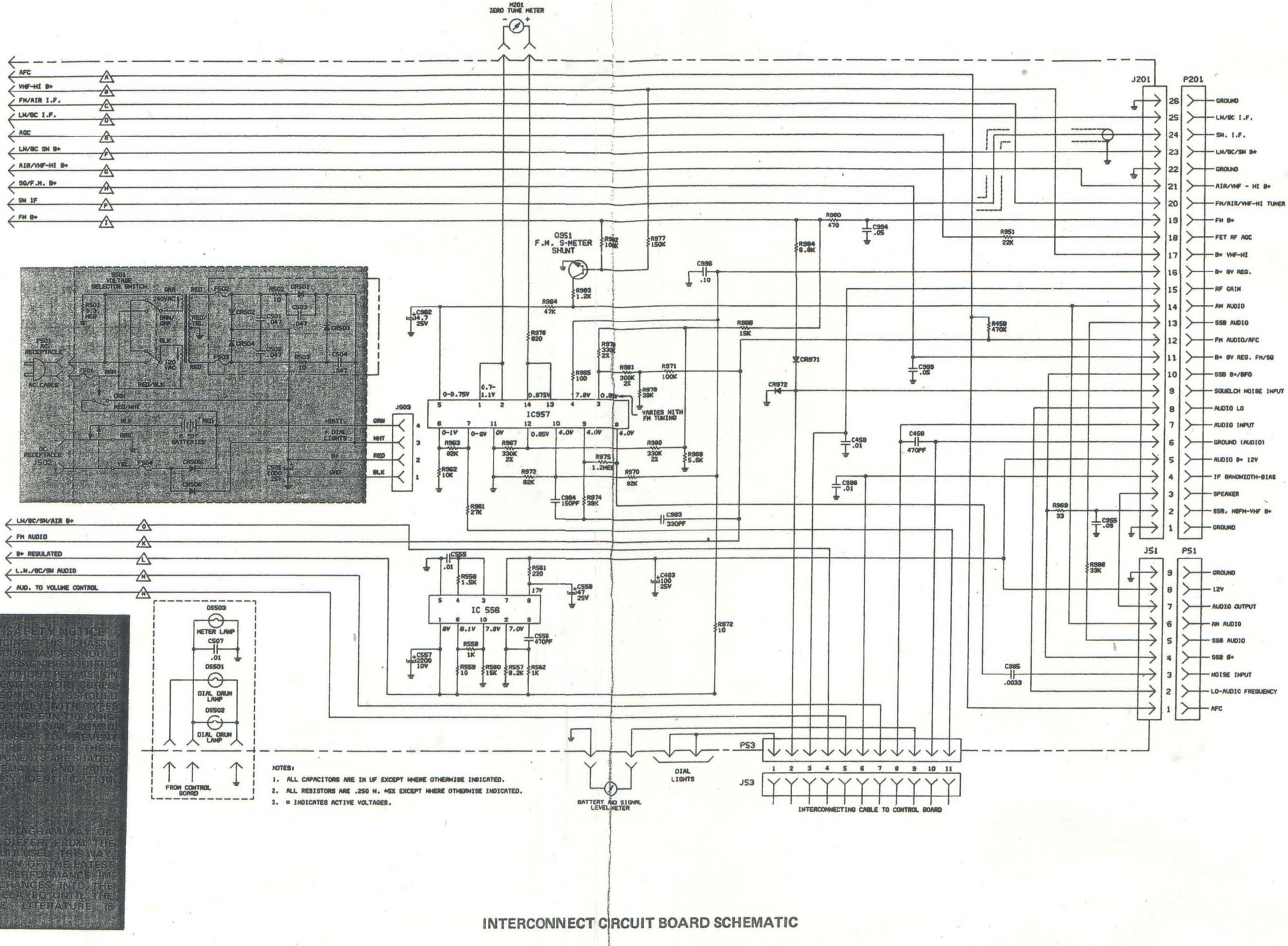
SIMPLIFIED SCHEMATIC



NOTE:
1. WAFERS S901C & S901D HAVE SPLIT RING
2. POSITION NUMBERS OF BANDSWITCH DO NOT CORRESPOND TO ACTUAL TERMINALS ON SWITCH

≈ RESISTORS FORM AN ATTENUATION NETWORK (CURRENTLY NOT USED) TO CONTROL OSCILLATOR AMPLITUDE (INJECTION)
≈≈ C966 NOT USED AT THIS TIME.

INTERCONNECT CIRCUIT BOARD SCHEMATIC



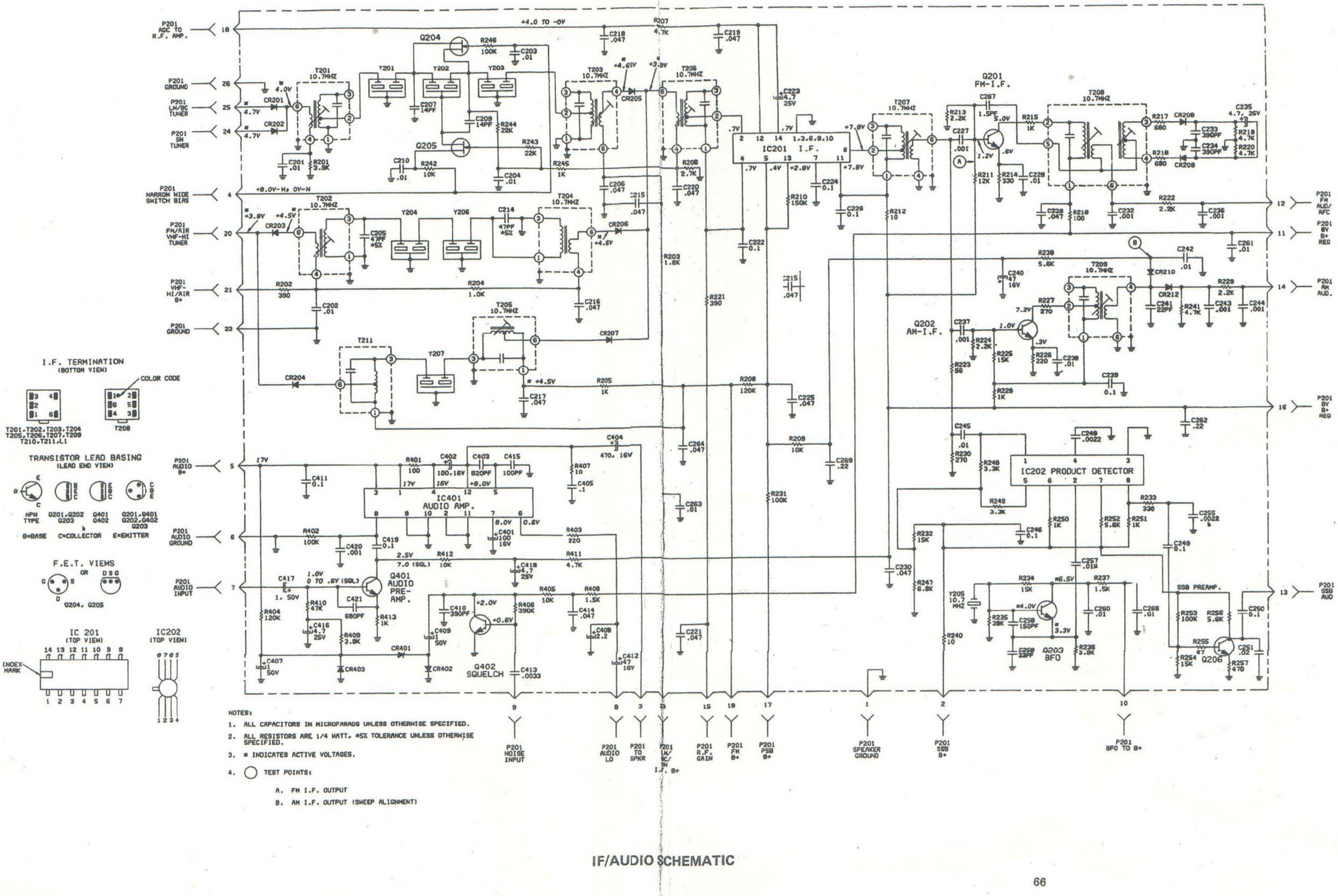
IMPORTANT SAFETY NOTICE

WHEN SERVICING THIS CHASSIS UNDER NO CIRCUMSTANCES SHOULD THE ORIGINAL DESIGN BE MODIFIED OR ALTERED WITHOUT PERMISSION FROM THE ZENITH CORPORATION. ALL COMPONENTS SHOULD BE REPLACED ONLY WITH TYPES IDENTICAL TO THOSE IN THE ORIGINAL CIRCUIT. SPECIAL COMPONENTS ARE USED TO PREVENT SHOCK AND FIRE HAZARDS. THESE CRITICAL COMPONENTS ARE SHADED ON THE SCHEMATIC AND PARTS LIST FOR EASY IDENTIFICATION.

THIS CIRCUIT DIAGRAM MAY OCCASIONALLY DIFFER FROM THE ACTUAL CIRCUIT USED. THIS WAY, IMPLEMENTATION OF THE LATEST SAFETY AND PERFORMANCE IMPROVEMENT CHANGES INTO THE SET IS NOT DELAYED UNTIL THE NEW SERVICE LITERATURE IS PRINTED.

NOTES:
1. ALL CAPACITORS ARE IN UF EXCEPT WHERE OTHERWISE INDICATED.
2. ALL RESISTORS ARE .25Q W. +5% EXCEPT WHERE OTHERWISE INDICATED.
3. = INDICATES ACTIVE VOLTAGES.

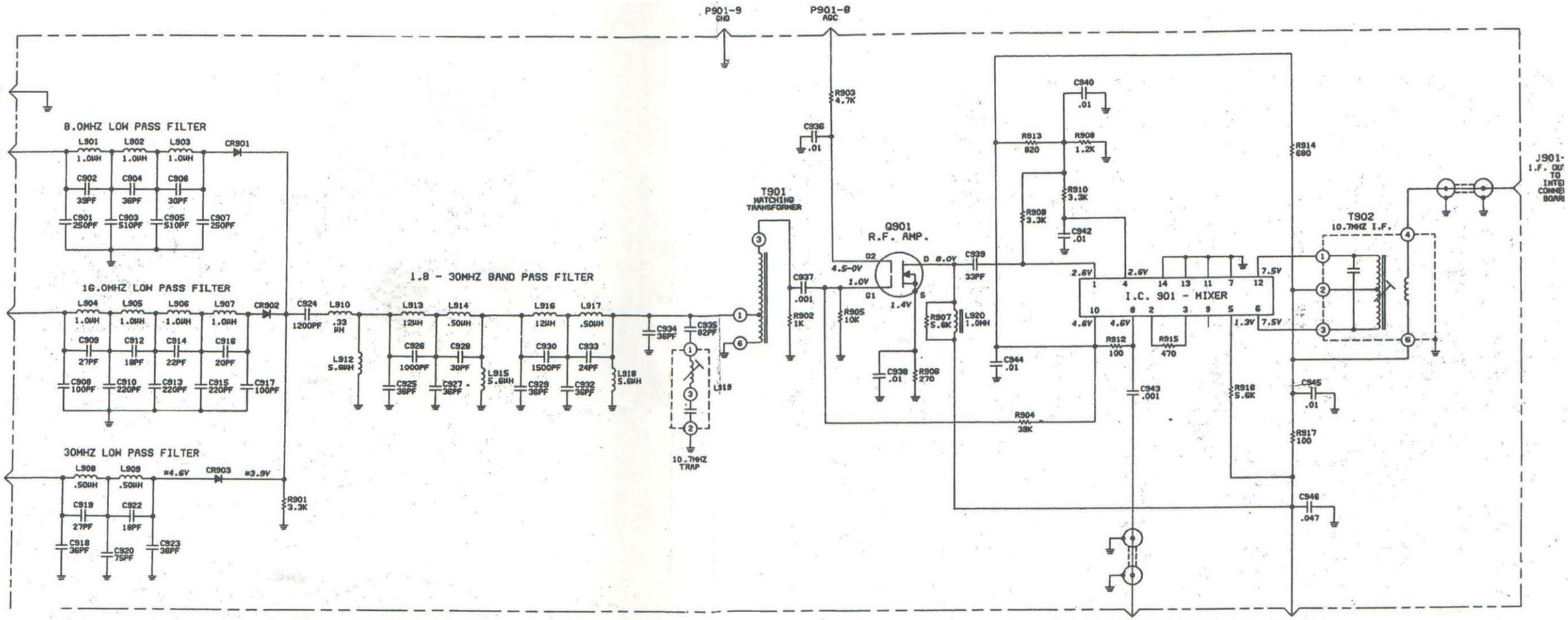
INTERCONNECT CIRCUIT BOARD SCHEMATIC

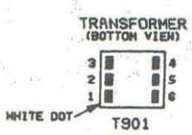


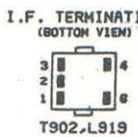
NOTES:

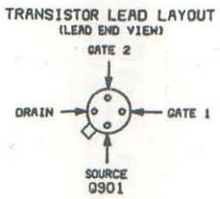
1. ALL CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
2. ALL RESISTORS ARE 1/4 WATT, ±5% TOLERANCE UNLESS OTHERWISE SPECIFIED.
3. # INDICATES ACTIVE VOLTAGES.
4. ◯ TEST POINTS:
   - A. FM I.F. OUTPUT
   - B. AM I.F. OUTPUT (SWEEP ALIGNMENT)

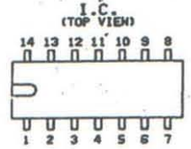
IF/AUDIO SCHEMATIC







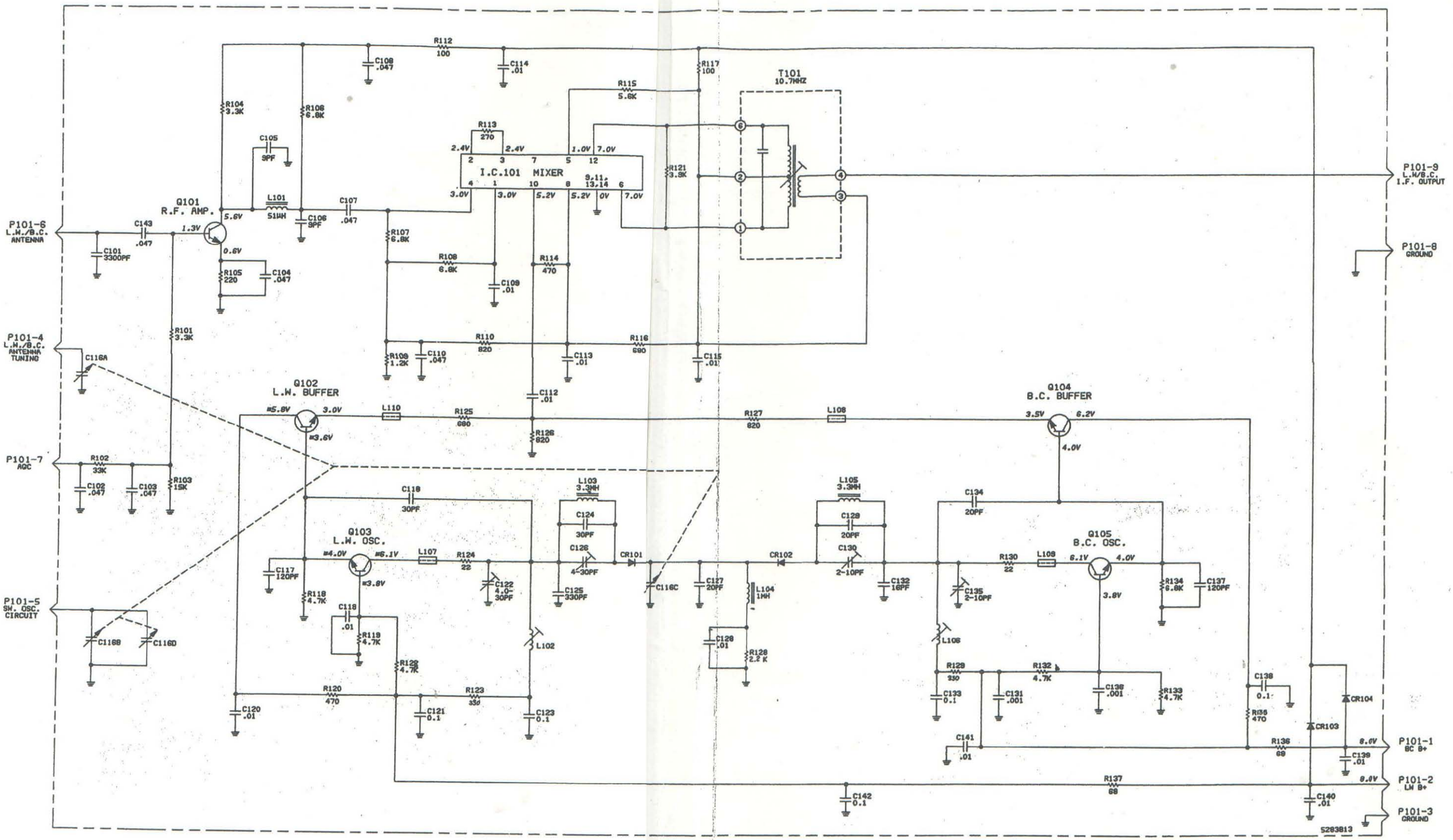




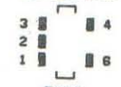
NOTES:

1. ALL CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
2. ALL RESISTORS ARE 1/4 WATT, ±5% TOLERANCE UNLESS OTHERWISE SPECIFIED.
3. ≈ INDICATES ACTIVE VOLTAGES.

SHORTWAVE TUNER SCHEMATIC



I.F. TERMINATION (BOTTOM VIEW)



T101

TRANSISTOR LEAD LAYOUT (LEAD END VIEW)

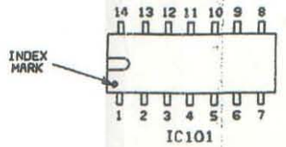
Q101,Q102,Q103
Q104,Q105

B = BASE

C = COLLECTOR

E = EMITTER

I.C. (TOP VIEW)

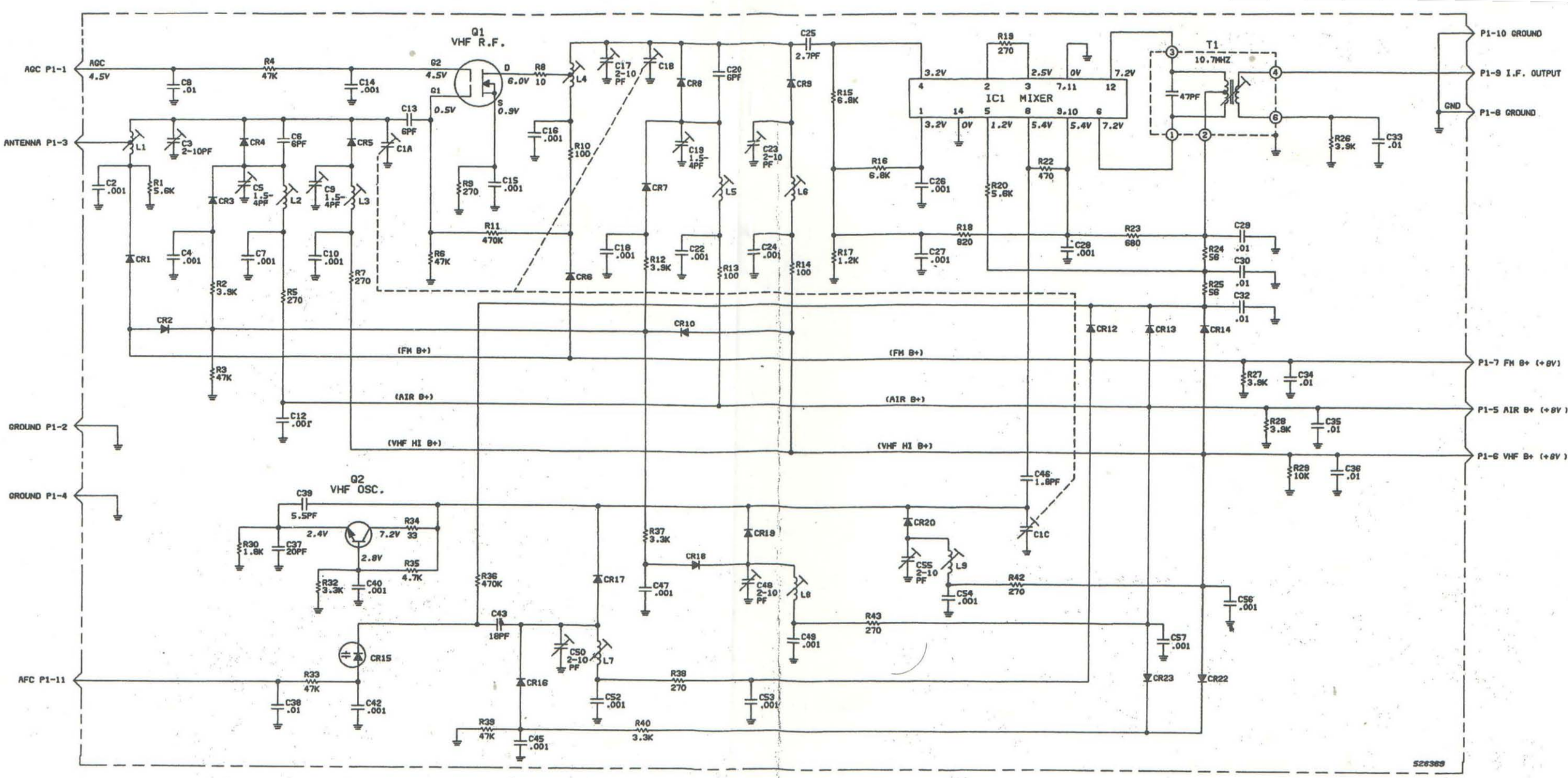


IC101

NOTES:

1. ALL CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
2. ALL RESISTORS ARE 1/4 WATT, 45% TOLERANCE UNLESS OTHERWISE SPECIFIED.
3. ≡ INDICATES ACTIVE VOLTAGES.

LW/BC TUNER SCHEMATIC



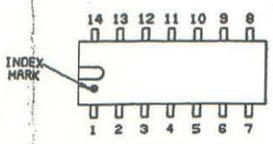
TRANSISTOR LEAD LAYOUT
(LEAD END VIEW)

Q2
NPN TYPE

B = BASE
C = COLLECTOR
E = EMITTER

Q1
F.E.T. TYPE

D = DRAIN
G1 = GATE1
G2 = GATE2
S = SOURCE



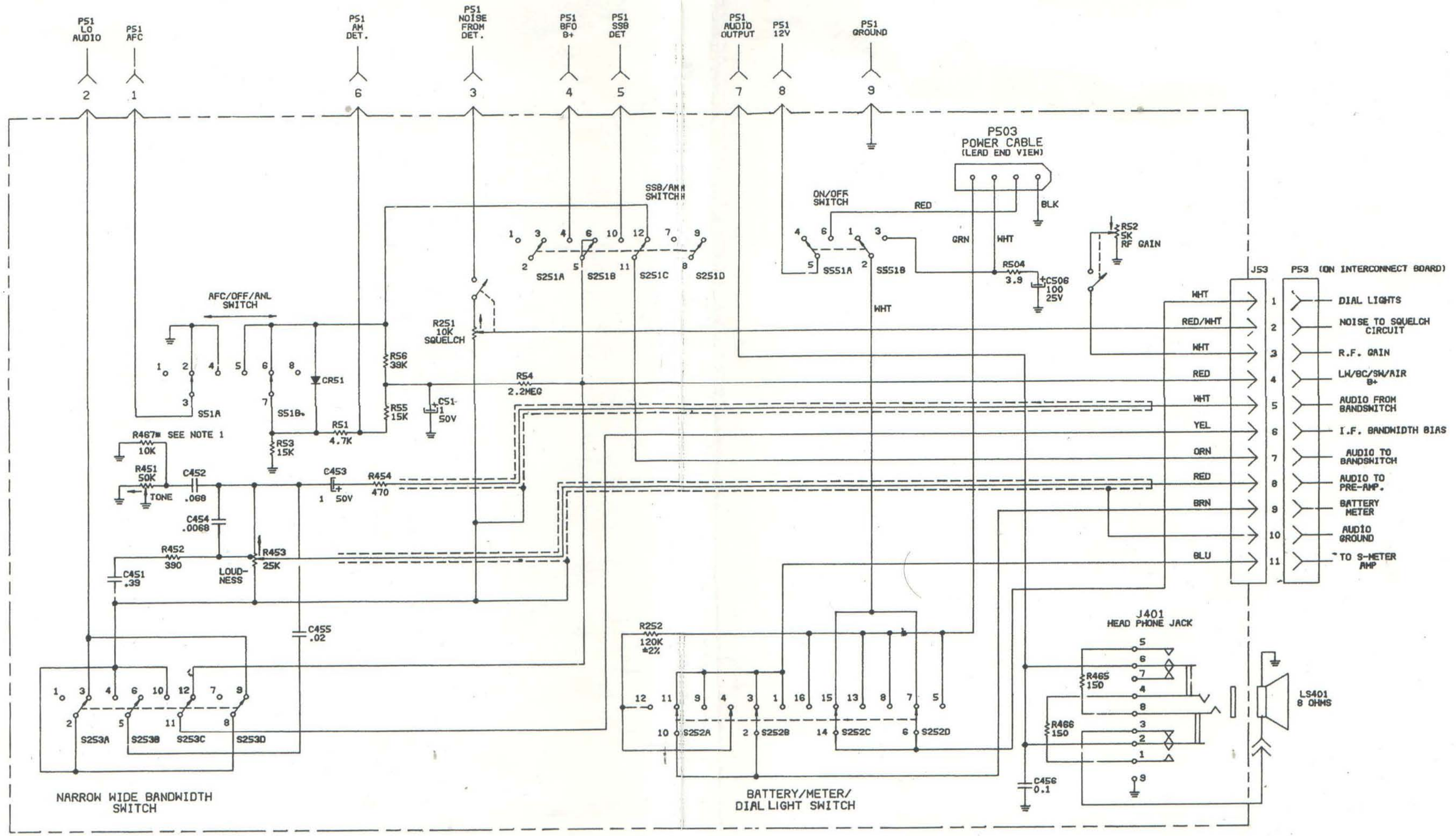
IC 1
(TOP VIEW)

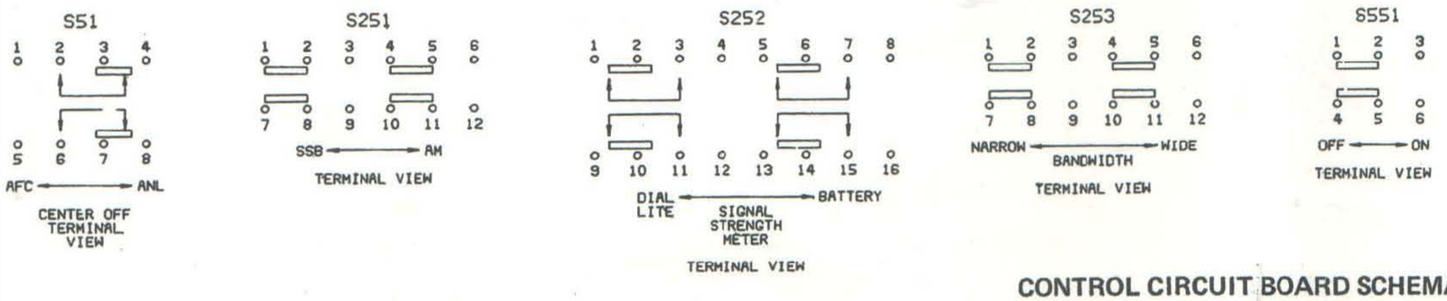
I.F. TERMINATION
(BOTTOM VIEW)

NOTES:

1. ALL CAPACITORS IN MF UNLESS OTHERWISE SPECIFIED.
2. ALL RESISTORS ARE .250 WATTS, ±5% TOLERANCE UNLESS OTHERWISE SPECIFIED.
3. ALL VOLTAGES INDICATED ARE WHEN BAND IS ACTIVE.

FM/AIR/VHF TUNER SCHEMATIC





CONTROL CIRCUIT BOARD SCHEMATIC